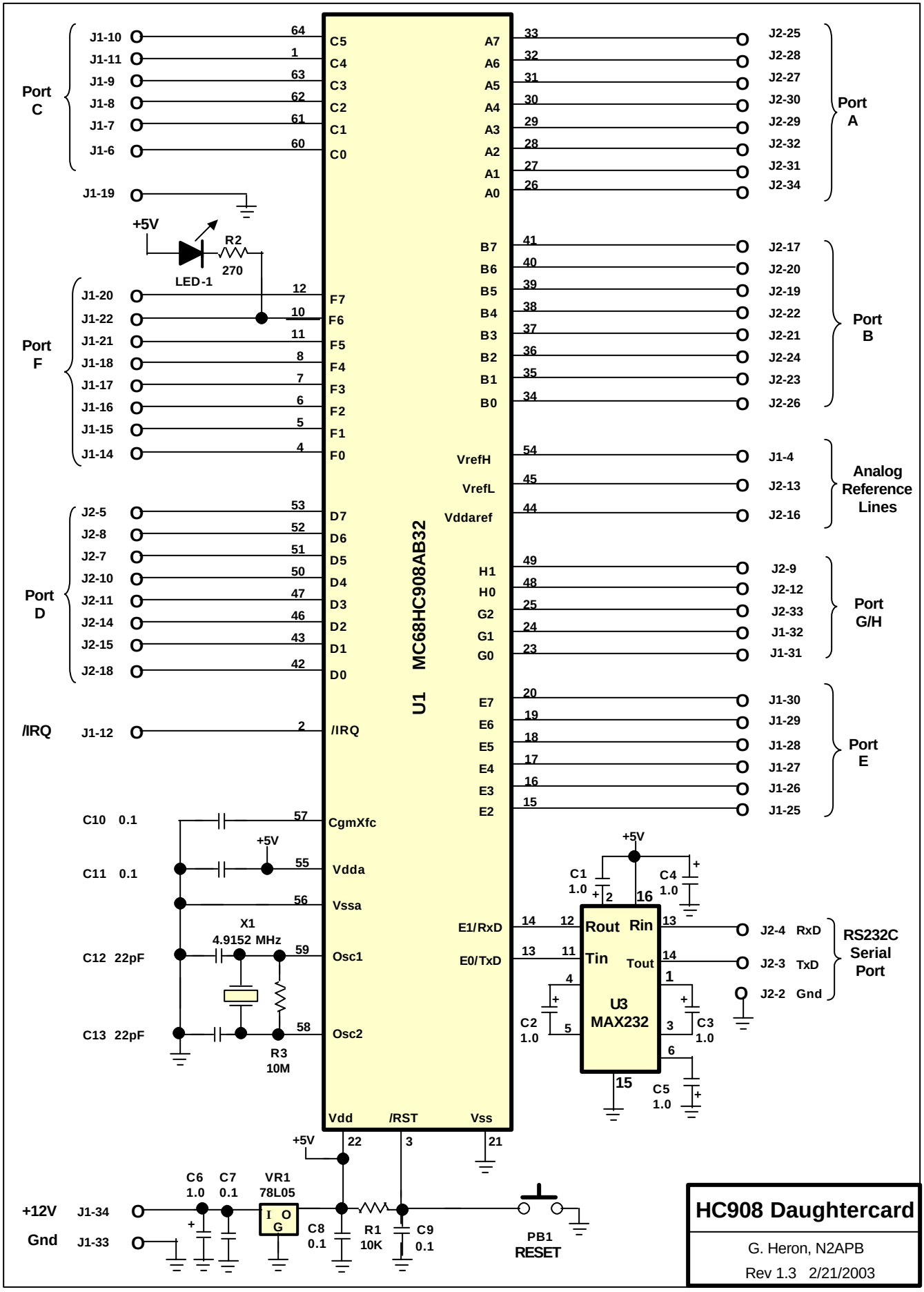
HC908 Daughtercard
G. Heron, N2APB
Rev 1.3 2/21/2003
U1 MC68HC908AB32
Port C
J1-10 64 C5
J1-11 1 C4
J1-9 63 C3
J1-8 62 C2
J1-7 61 C1
J1-6 60 C0
J1-19
+5V
R2 270
LED-1
Port F
J1-20 12 F7
J1-22 10 F6
J1-21 11 F5
J1-18 8 F4
J1-17 7 F3
J1-16 6 F2
J1-15 5 F1
J1-14 4 F0
Port D
J2-5 53 D7
J2-8 52 D6
J2-7 51 D5
J2-10 50 D4
J2-11 47 D3
J2-14 46 D2
J2-15 43 D1
J2-18 42 D0
/IRQ J1-12 2 /IRQ
C10 0.1 57 CgmXfc
C11 0.1 +5V 55 Vdda
56 Vssa
X1 4.9152 MHz
C12 22pF 59 Osc1
C13 22pF 58 Osc2
R3 10M
Vdd /RST Vss
+5V 22 3 21
C6 1.0
C7 0.1
VR1 78L05
I O G
+12V J1-34
Gnd J1-33
C8 0.1
R1 10K
C9 0.1
PB1 RESET
Port A
A7 33 J2-25
A6 32 J2-28
A5 31 J2-27
A4 30 J2-30
A3 29 J2-29
A2 28 J2-32
A1 27 J2-31
A0 26 J2-34
Port B
B7 41 J2-17
B6 40 J2-20
B5 39 J2-19
B4 38 J2-22
B3 37 J2-21
B2 36 J2-24
B1 35 J2-23
B0 34 J2-26
Analog Reference Lines
VrefH 54 J1-4
VrefL 45 J2-13
Vddaref 44 J2-16
Port G/H
H1 49 J2-9
H0 48 J2-12
G2 25 J2-33
G1 24 J1-32
G0 23 J1-31
Port E
E7 20 J1-30
E6 19 J1-29
E5 18 J1-28
E4 17 J1-27
E3 16 J1-26
E2 15 J1-25
+5V
C1 1.0
C4 1.0
16
E1/RxD 14 12 Rout Rin 13 J2-4 RxD
E0/TxD 13 11 Tin Tout 14 J2-3 TxD
1 J2-2 Gnd
4
C2 1.0
5
U3 MAX232
3 C3 1.0
6
15
C5 1.0
RS232C Serial Port